About NT Silchar:

National Institute of Technology Silchar is one of the 31 National Institutes of Technology of India and was established in 1967 as a Regional Engineering College Silchar in Assam. In 2002, it was upgraded to the status of National Institute of Technology and was declared as Institute of National Importance under the National Institutes of Technology Act, 2007. NIT Silchar is a fully residential campus situated on the banks of river Barak and on a sprawling green campus spread over 625 acres of land surrounded by scenic tea gardens on the outskirts of Silchar. NIT Silchar is a teaching and research institute which reflects in the top NIRF ranking. In 2020 NIRF ranking, NIT Silchar secured 9th among all NITs, 46th rank in engineering and 94th in overall category. NIT Silchar has been ranked INTERNATIONALLY FOR THE VERY FIRST TIME by the highly reputed Times Higher Education(THE) World University Ranking 2021 with a rank in the band of 801-1000. NITS ranked in band of 11-25th rank in ATAL RANKING OF INSTITUTIONS ON INNOVATION ACHIEVEMENTS 2020.



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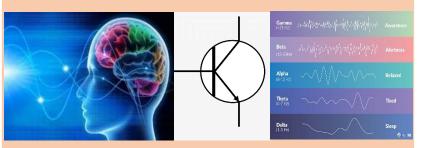
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Five-Days Webinar and Faculty Development Program (FDP) on "Analog/Mixed VLSI Circuits for Brain - Machine Interface"

12th -16th October-2020



Organized By

Department of Electronics and Communication Engineering

National Institute of Technology Silchar, Assam-788010, India

(An Institute of National Importance under MHRD, Govt. of India)







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About ECE Department:

The department offers B.Tech in Electronics and Communication Engineering and M.Tech in two specializations (i) Micro-Electronics and VLSI Design & (ii) Communication and Signal Processing Engineering. This department also offers a Ph.D. in Electronics and Communication Engineering. The state of art laboratory facilities exists in the department to train the UG, PG, and Ph.D. scholars to carry research in the frontier research areas of Electronics and Communication Engineering. The faculty members are specialized in diverse fields, and there is a commendable research ambience in the department. Department of Electronics and communication Engineering takes up R & D projects sponsored by various funding agencies such as DST, SERB, BARC, MeitY CSIR, and many more.

About Webinar

The webinar's objectives are to bring the researchers and technocrats from different parts of our country to a common gathering for exchanging and sharing knowledge about the recent developments in the area of Analog and Mixed VLSI for Brain Machine interface. It covers designs issues and challenges for Bio applications. Analysis of Biomedical Signals, Modeling and design of VLSI Circuits for certain Biological systems like cochlear, human vision system, Brain machine interface etc. VLSI Signal Processing in context to Bio-Signals. VLSI Building blocks Comparators, ADC/DAC, Analog multiplier Design of Phase locked loop and challenges Design of Active-RC, MOS-C, Gm-C, CTI, SC filter circuits for Biomedical systems design.

Important Dates:

Last Date of accepting applications: 31st Sept 2020

Notification of Selection: 5th Oct 2020

Date of Workshop: 12th to 16th Oct 2020

Five-Days Webinar and Faculty
Development Program (FDP) on
"Analog/Mixed VLSI circuits for Brain
Machine interface"

12th -16th October-2020

Registration Form

Ι.	Name
2.	Designation
	Organization
4.	Address
	Phone/Mobile
	Email:
7.	Qualification

Declaration by applicant:

The information furnished in the application is true to best of my knowledge.

Date:

Signature

Contact us:

Dr. K.L.Baishnab

Email: klb@ece.nits.ac.in

Phone: 9435176506/8011884756

Dr. Koushik Guha

Email: koushik@ece.nits.ac.in

Phone: 9435072274

Topics to be covered:

- Challenges in design of Analog VLSI Designs for Bio applications
- 2. Biomedical Systems Models with case studies
- 3. Design of Gm-C filter, Preamplifier, buffer, ADC, etc.
- 4. Machine learning for interpreting brain signals

Tentative Speakers:

Prof. Jayanta Mukerjee, IIT Bombay

Prof. Sudeb Dasgupta, IIT Roorkee

Prof. Roy P Paily, IIT Guwahati

Dr S. Mitra Univeristy of Edinburgh

Dr S. Purkayastha Neuro interventionist

Dr. Bakul Goel, DAIICT Ghandhinagar

Prof. Nidul Sinha NIT Silchar

Dr. K L Baishnab, NIT Silchar

Dr. Koushik Guha, NIT Silchar

Dr. Libu Manjakkal (University of Glasgow)

Dr. Debajit Basak, PDF, HKUST, HongKong

Mr Amlan Nag, Univeristy of Edinburgh

Mr. F. F Franco University of Glasgow

Mr. Saoirse Dervin University of Glasgow)

Mr. A K M Delwar Hossain, Synopsys, Canada

Eligible Participants:

The faculty members of the AICTE approved institutions, research scholars, PG Scholars, participants from Government, Industry (Bureaucrats/Technicians/Participants from Industry etc.) and staff of host institutions.

Registration: Free

Link for online registration

https://atalacademy.aicte-india.org/