

## Convenor

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# 15 Days

**HANDS-ON TRAINING  
ON VLSI CIRCUIT DESIGN  
USING CADENCE**



**Organised By**

**Department of ECE**  
**National Institute of Technology, Silchar**  
*(An Institute of National Importance)*



## About The Institute

National Institute of Technology Silchar is one of the 30 National Institutes of Technology of India and was established in 1967 as a Regional Engineering college in Assam. In 2002 it was upgraded to the status of National Institute of Technology and was declared as Institute of National Importance under the national institute of technology act, 2007.

## About The Department

The Department of Electronics & communication offers B. Tech program in Electronics & Communication Engineering covering modern communication and software technologies. Keeping in mind the great potential and demand for new communication technologies, the Department aims at imparting its students with the latest knowledge in the field. To complement this the Department has well stocked laboratories housing various modern equipment's to enable the students to keep pace with the fast changing technology.

## About the Training

The training is intended to provide a vibrant opportunity for research scholars to enrich their knowledge in the area of analog/digital IC Design. The following topics will be covered on this training.

- ◆ Review of Analog/Digital VLSI Design
- ◆ Frontend Design & Simulation with all possible analysis
- ◆ Backend Design followed by DRG/LVS & PEX/RCX
- ◆ Monte-Carlo Analysis
- ◆ GDSII file extraction(input/output)
- ◆ Introduction to SoC Enclosures/Innovata
- ◆ Introduction to Chip Tapeout with SCL 180nm Technology

## Important Dates

- ◆ Starting Date-01-06-2018.
- ◆ Registration is open from 20<sup>th</sup> March 2018.
- ◆ Last date for the application receipt: 25<sup>th</sup> April 2018.
- ◆ Seat is limited up to 10 participants, & can be accommodated on first come first serve basis.
- ◆ Confirmation mail will be sent after 25<sup>th</sup> April 2018.

## Accommodation

Hotel accommodation will be provided with nominal charge.

## Registration

Registration fee of Rs.10,00000 INR.

## Mode of Payment

- ◆ DD of Rs.10,000.00 in favour of Director NIT Silchar  
Payable at SEI NIT branch.  
Branch Code-7061.
- ◆ Online payment can be made through this channel

**In favour of Director NIT Silchar**

A/C No.10521277057

IFSC Code:SBIN0007061

MICR Code:788003004

Send the duly signed application with payment receipt to  
Dr. K.L. Baidnab, Coordinator  
Email-klbaidnab@gmail.com/klb@ecv-nit.ac.in