### **About NIT Silchar**

National Institute of Technology (NIT) Silchar, an Institute of National Importance under the NIT Act was established in 1967 as Regional Engineering College (REC) Silchar in Assam. In the year 2002, it was upgraded to the status of an NIT from REC. NIT Silchar is situated on the banks of river Barak and on a sprawling campus spread over 600 acres of land on the outskirts of Silchar. NIT Silchar is a fully residential Institution. At present it offers six undergraduate courses in Civil, Electrical, Mechanical, Electronics & Communication, Computer Science & Engineering and Electronics & Instrumentation Engineering. All the departments of the Institute also offer M. Tech. and Ph. D. programmes.

### **About the Department**

The department offers core courses in Power Systems, Control Systems, Electrical Machines, Instrumentation, Switchgear and Industrial Protection, Microprocessors and Microcontrollers etc. The state-of-the-art Lab facility exists in the department like Machine Lab, Power Systems Lab, Advance Control Lab Microprocessor & Embedded System Lab and Computational Lab, etc. to name a few. Various research and development activities are taken up by the department to put the students on busy schedules enabling them to blend to the modern industrial requirements. The department regularly organizes seminars, workshops, conferences at national and international level.

### Eligibility

This program is open to faculty members, industrial personnel and students including research scholars, PG and UG students of recognized technical institutions.

#### **Accommodation and Travel**

A limited numbers of out station participants will be provided free accommodation in the Institute Guest House subject to the availability. No TA and DA will be paid to the participants.

### **Address for Communication**

Dr. L C Saikia, Dr. D Koteswara Raju & Dr. Tapan Pradhan, Assistant Professor, EED NIT Silchar. E-mail: lcsaikia@yahoo.com,

eswaar.raju@gmail.com & tapanpradhan78@gmail.com Contact No.: +919435143835, +9170883803445 & +919432125545.

### About the Course

The workshop on "Synchrophasor Measurement Technology for Smart Grid - Present Scenario and Implementation in Indian Power System" is scheduled to be held during 19<sup>th</sup>-23<sup>rd</sup> January 2019. Experts from academia and industries are invited as resource persons. This course is an excellent opportunity for PG, Ph. D scholars, industry personals and final year UG students to work on industry relevant power system problems. The course is designed to provide a foundation for future research in the field of power systems.

# One Week Workshop On

Synchrophasor Measurement Technology for Smart Grid -Present Scenario and Implementation in Indian Power System

(Under TEQIP-III)

19th -23rd January, 2019

Coordinators

#### Dr. L C Saikia Dr. D Koteswara Raju Dr. Tapan Pradhan

Organized By:



Electrical Engineering Department, NIT Silchar

## **Course Objectives**

The basic objective of this workshop is to train the students, researchers and industry practitioners to use professional power system software to solve practical power system problems. Following topics are expected to be covered in the workshop:

- ✤ Introduction to WAMS.
- ✤ Basics of WAMS and PMU.
- ✤ Application of PMU.
- ✤ International and National scenario.
- Merits and demerits.
- ✤ Current research.
- Future research possibilities.

## **Tentative Speakers**

- 1. Prof. Sukumar Mishra, IIT Delhi,
- 2. Prof. Sandip Ghosh, IIT BHU,
- 3. Prof. P. Tripathy, IIT Guwahati,
- 4. Prof. S. Dalai, Jadavpur University,
- 5. Industry Expert from PRDC.

## Committee

- *Chief-Patron* Prof. Sivaji Bandyopadhyay, Director, NIT Silchar
- PatronsProf. N. B. Dev Choudhury<br/>HOD, EED, NIT Silchar

Dr. P. J. Roy Coordinator, TEQIP-III

#### Advisory Committee

Prof. N. Sinha Prof. B. K. Roy

Prof. S. Choudhury

#### **Organizing Committee**

Dr. D. C. Das Dr. T. Malakar Dr. P. K. Tiwari Dr. R. Dey Dr. R. K. Biswas Dr. P. Kayal Dr. S. Ray Dr. A. Pati

Dr. J. P. Mishra Dr. C. Bhattacharjee Dr. N. Soren Dr. P. Roy Dr. A. Kumar Dr. N. Adhikari

Dr. A. K. Goswami

Dr. L. C. Saikia Dr. D. Koteswara Raju & Dr. T. Pradhan (Coordinators)

## Registration

All participants have to pay registration fees before submitting application either (i) via DD in favour of Director NIT Silchar, Payable at State Bank of India, NIT Silchar branch or (ii) online transfer to the account of the Director, NIT Silchar (A/C No.: 10521277057, Branch: NIT Silchar). Brochure and registration form can be downloaded from the institute website: http://www.nits.ac.in.

## **Fees Details**

Industry Persons:	Rs. 5000/-
External Faculty members:	Rs. 3000/-
External Students:	Rs. 1000/-
Internal Faculty members:	Rs. 1000/-
Internal Students (PG & PhD) :	Rs. 500/-
Internal Students (UG)	Free

## **Submission Guideline**

Scan copy of the signed registration form to be sent to the coordinators through e-mail on or before 14<sup>th</sup> January, 2019. However, original money receipt has to be submitted at the time of registration along with the registration form.

#### **REGISTRATION FORM**

One Week Workshop On

Synchrophasor Measurement Technology for Smart Grid - Present Scenario and Implementation in Indian Power System (Under TEQIP-III)

19<sup>th</sup> – 23<sup>rd</sup> January, 2019

1. Name (block letter):

2. Designation:

3. Organization:

4. Address for communication:

- 5. Ph. No.:
- 6. E-mail:
- 7. Educational Qualification:
- 8. Accommodation Required: Yes / No
- 9. Payment Details:

(DD/receipt no)

Place:

Date:

Signature of the applicant .....

Recommended by HOD/ HOS

(Signature & Seal)